

Reasoning About a Machine with Local Capabilities

Provably Safe Stack and Return Pointer Management

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ESOP, April 17, 2018

What Does This Program Do?

```
let x = ref 0 in
  λf.(x := 0;
      f();
      x := 1;
      f();
      assert (!x == 1))
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    f();
    x := 1;
    f();
    assert (!x == 1)
  in
```

1. Consider program. Assuming a standard ML semantics we can say what it does.
2. Bind x to freshly allocated reference in a closure that...
3. takes callback f , sets x to 0, calls f , sets x to 1, calls f and finally asserts x points to 1.
4. Note the assumption that when we call f , then we return to a specific program point. This is what we call well-bracketedness and we assume we have this in many programming languages.
5. However, in order to execute this code, we need to compile it to assembly.
6. How is well-bracketedness guaranteed? In particular, how is it guaranteed if f is a piece of code we do not trust (maybe handwritten assembly).

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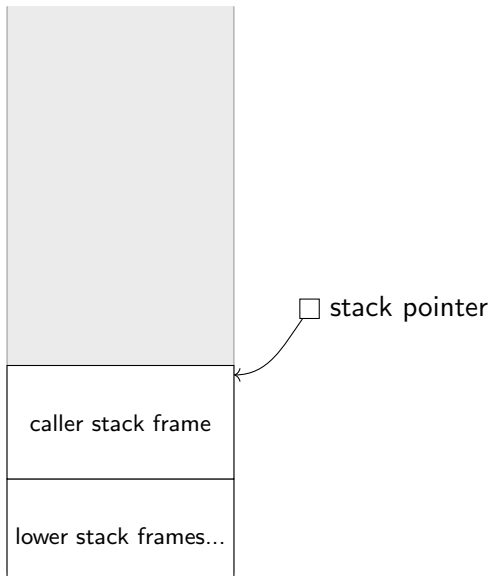
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1. We present a calling convention for capability machines that provide well-bracketedness and local state encapsulation as well as a logical relation that allows us to reason about such programs.
2. Let's first consider how stack pointers traditionally are handled.

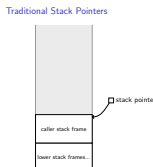
Traditional Stack Pointers



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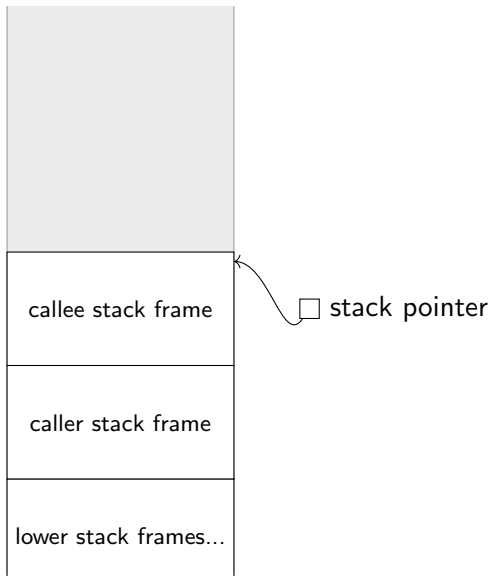
Reasoning About a Machine with Local Capabilities

Traditional Stack Pointers



1. Simply put, a caller calls a function which
2. pushes a new stack frame on the stack the callee uses for its execution.
3. When the callee is done, then it returns to the caller by popping its stack frames.

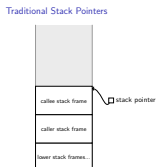
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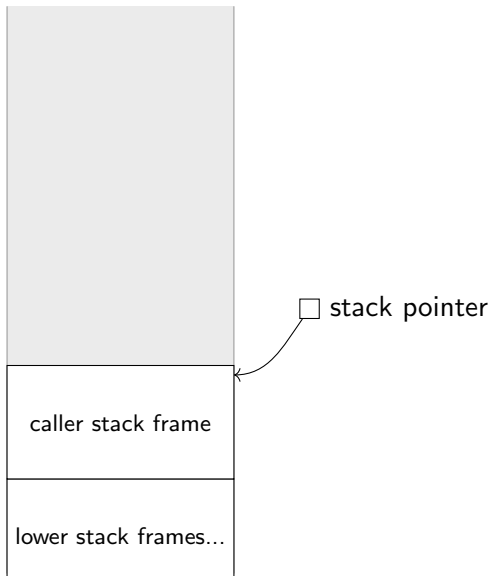
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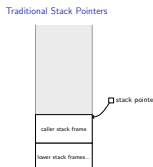
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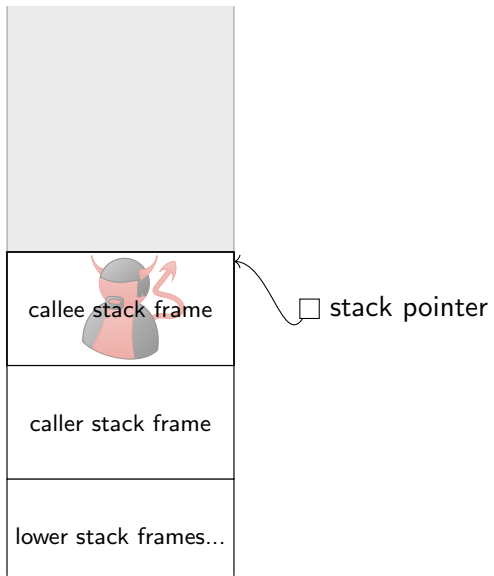
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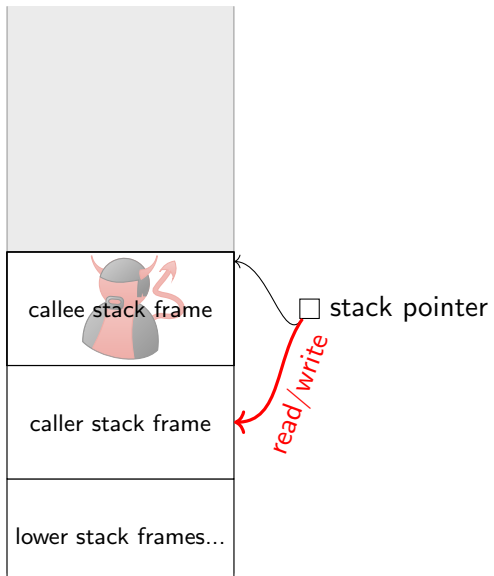
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1. If callee (evil) assembly code with no intention to follow the CC, then there are multiple ways for them to break things:

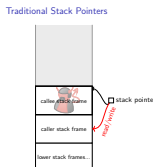
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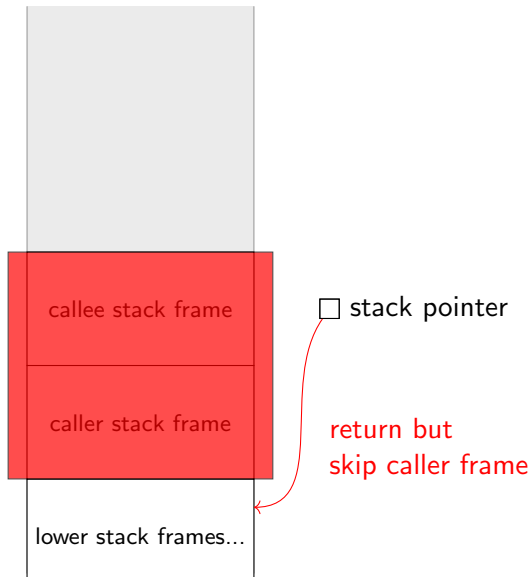
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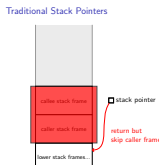
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Traditional Stack Pointers



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2. Read or write directly from or to the caller's stack frame, breaking local-state encapsulation
3. Skip the caller's stack frame and return to one further down breaking well-bracketedness.
4. Clearly we need some kind of low-level enforcement mechanism.

Capability Machine

- ▶ Low-level machine

Memory



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Capability Machine

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1. Capability machines are low-level machines proposed in the systems community.
2. For instance, the CHERI OS operates on one.
3. Has all the instructions we expect, load, store, jmp, etc.

Capability Machine

- ▶ Low-level machine
- ▶ Capabilities replace pointers

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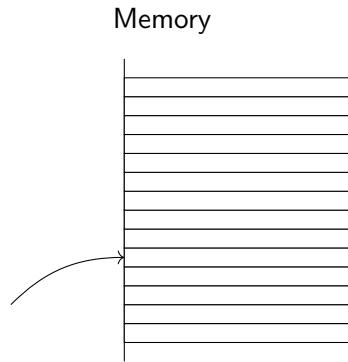
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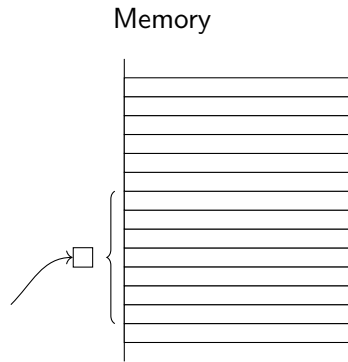
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Capability Machine

- ▶ Low-level machine
- ▶ Capabilities replace pointers
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 - ▶ Range of authority



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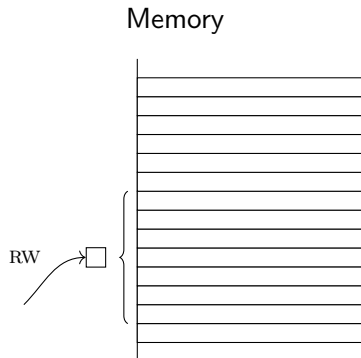
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Capability Machine

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- ▶ Capabilities replace pointers
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 - ▶ Range of authority
 - ▶ Kind of authority
 - ▶ read, write, and execute
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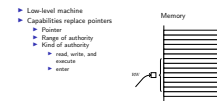


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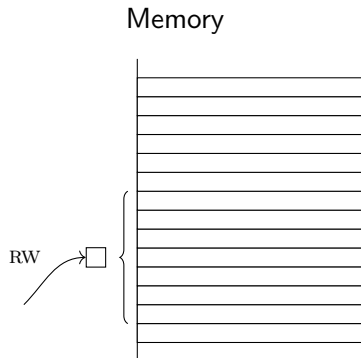
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6. Provides encapsulation mechanism which allows separation of security domains.
7. Can not be used for anything but jump, when jumped to becomes read/execute.

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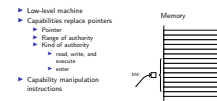


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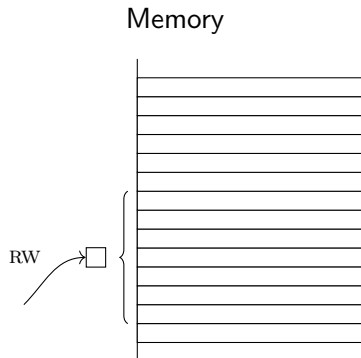
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- ▶ Capability manipulation instructions
- ▶ Authority checked dynamically



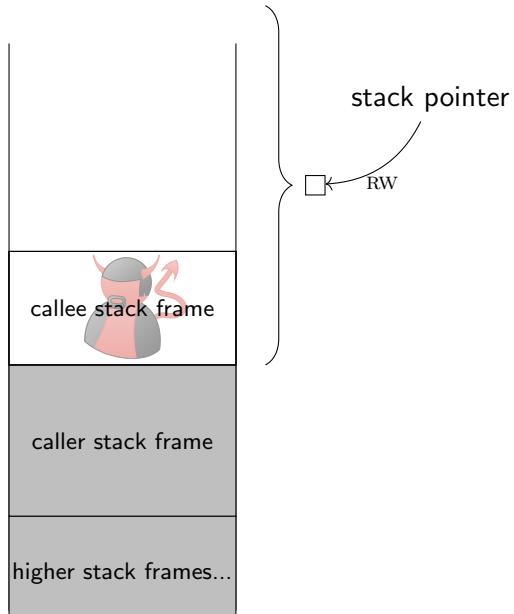
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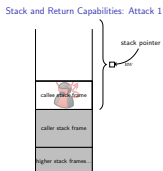
Stack and Return Capabilities: Attack 1



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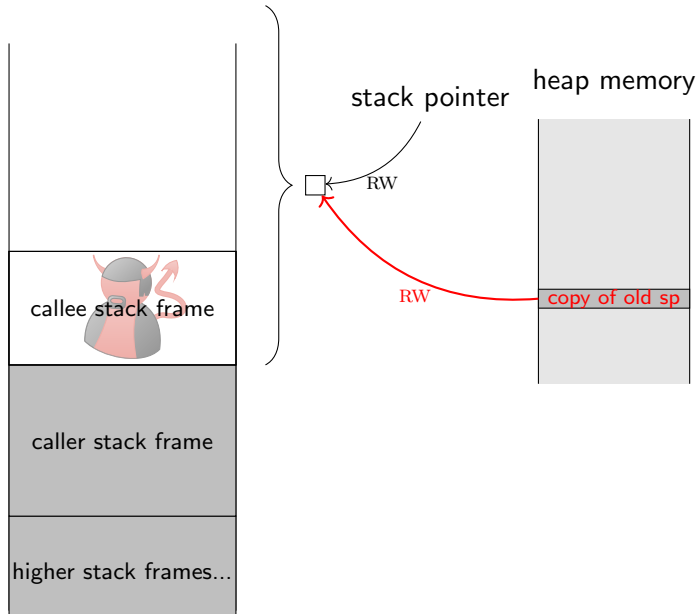
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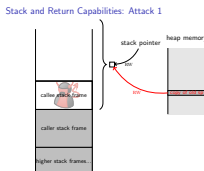
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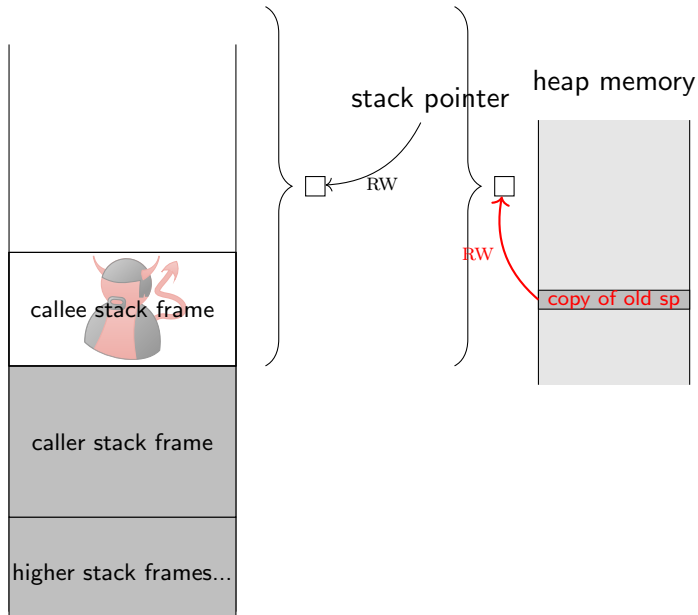
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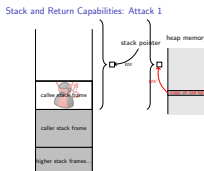
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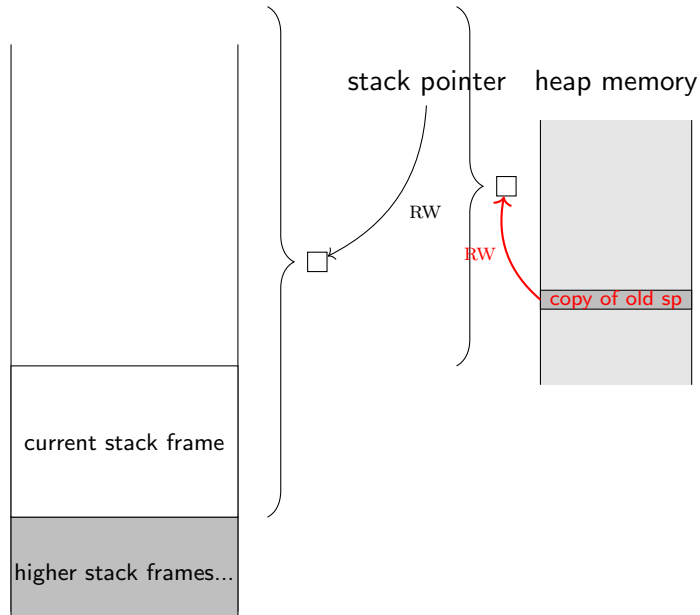
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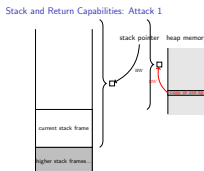
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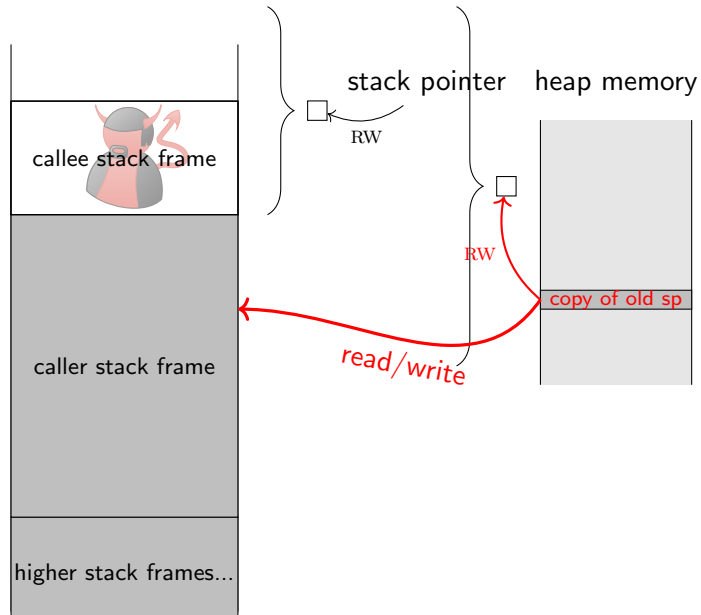
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6. Again breaking local state encapsulation.
7. Need a way to make sure stack pointer is not stored for later use.

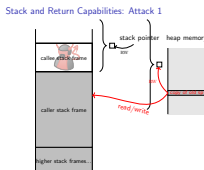
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Local Capabilities

CHERI inspired

- ▶ Capabilities tagged with locality (local or global)
- ▶ New *write-local* permission
- ▶ Local capabilities can only be stored by capabilities with *write-local* permission

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Reasoning About a Machine with Local Capabilities

- Local Capabilities

1. To revoke a capability, we need to find it in memory which means we need access + need to search the entire memory.
2. Restricted where local capabilities can be stored. restricts where we need to look for a capability.
3. We define a calling convention. In order to prevent attack 1, we do the following.

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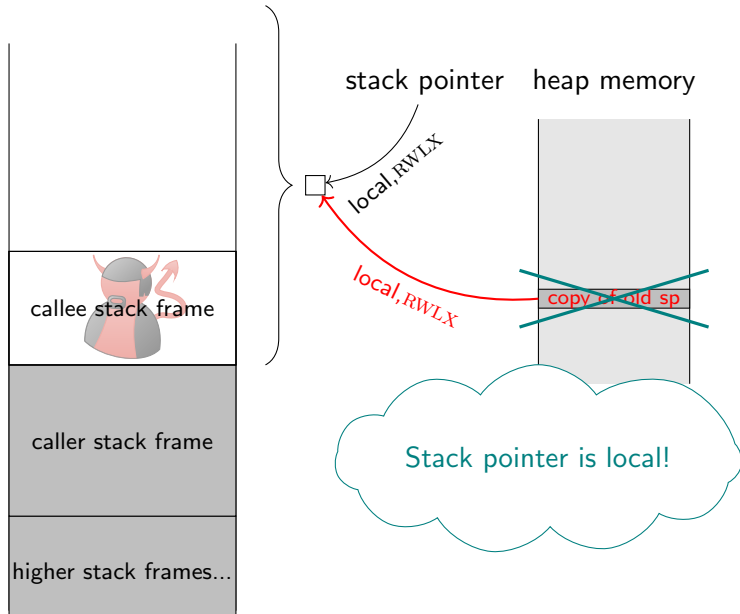
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Calling convention

- ▶ Stack capability is local with permission read, write-local, and execute.
- ▶ No global write-local capabilities.

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3. We define a calling convention. In order to prevent attack 1, we do the following.
4. Local stack capability cannot be stored on the heap. We need to be able to store old stack pointers somewhere, traditionally stack.
5. Global write-local capabilities would undermine the entire idea as it would allow local capabilities to be stored indirectly.

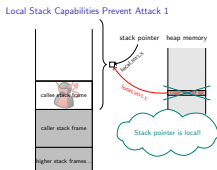
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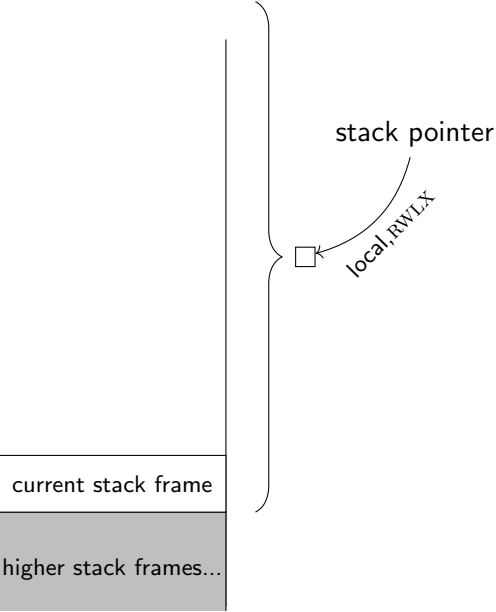
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Local Stack Capabilities Prevent Attack 1



In the attack from before, when the attacker attempts to store the stack capability on the heap, then the machine checks that we have the correct authority to perform the operation. Assuming we only have global capabilities for the heap, it cannot have write-local authority, due to the assumption on the previous slide, so we try to store the stack capability through a capability that does not have write-local authority, so it fails.

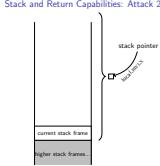
Stack and Return Capabilities: Attack 2



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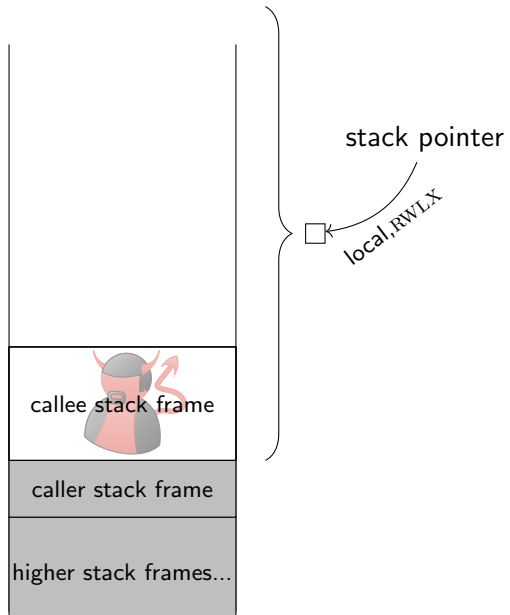
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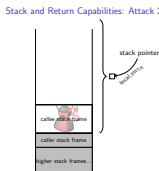
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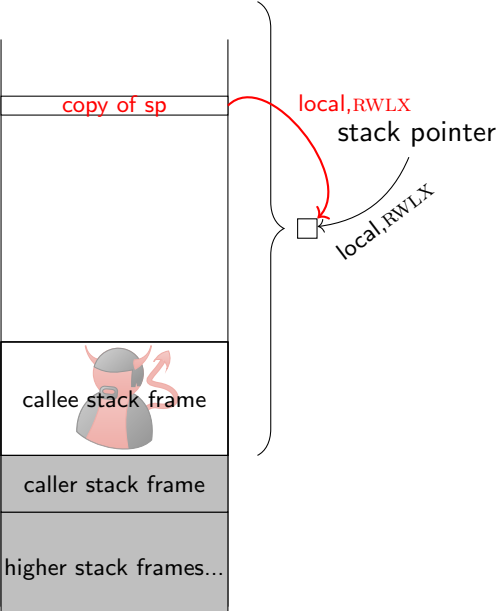
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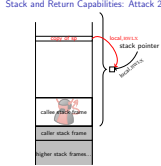
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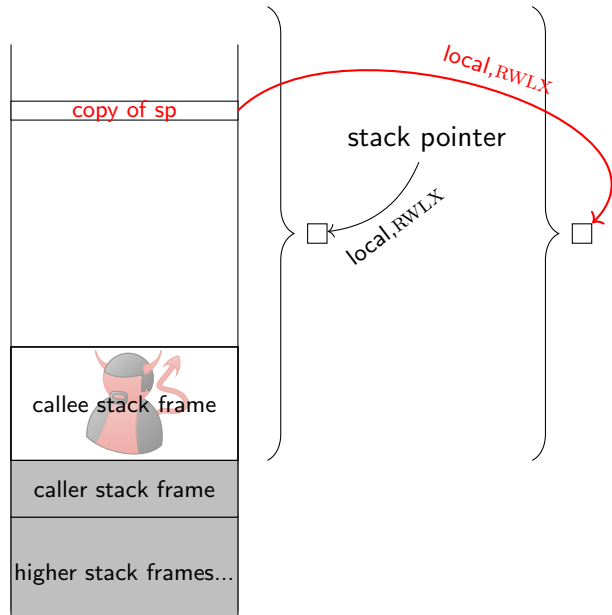
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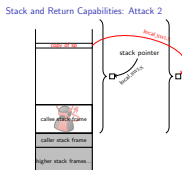
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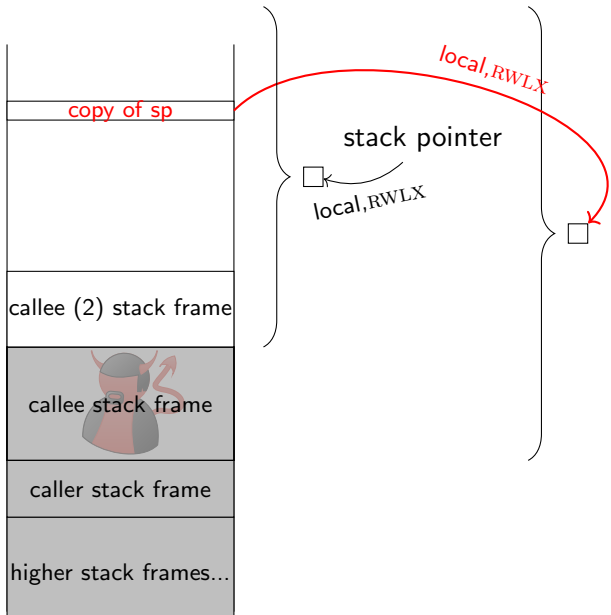
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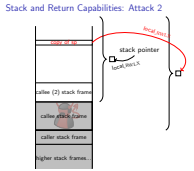
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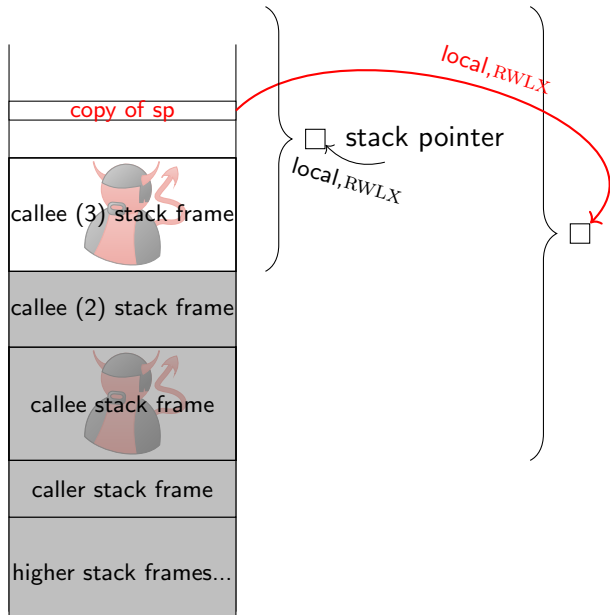
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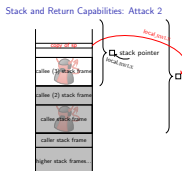
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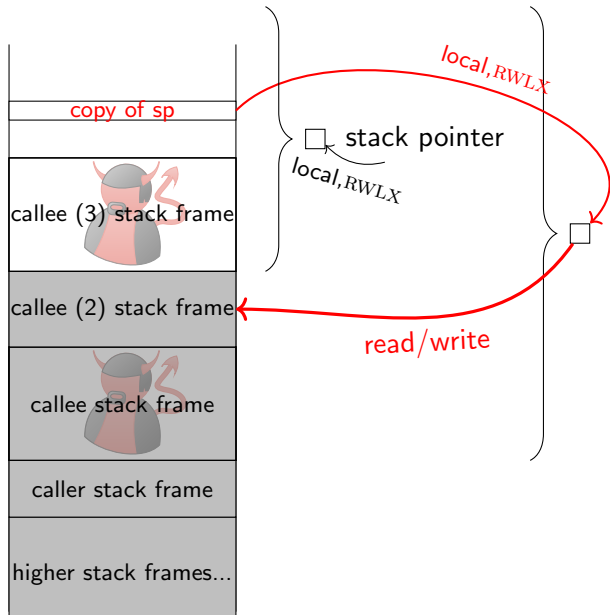
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7. The stack pointer is still on the stack allowing the untrusted code to read write to the stack frame of the trusted code.

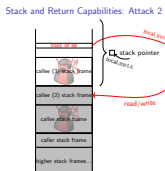
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4. stack pointer local, but stack pointer has write local permission, so no problem.
5. untrusted code calls some trusted code with a callback.
6. trusted code runs for a bit pushes some local data to the stack and calls the callback.
7. The stack pointer is still on the stack allowing the untrusted code to read write to the stack frame of the trusted code.

Calling Convention (Continued)

...

- ▶ Clear stack and non-argument registers before invoking untrusted code.

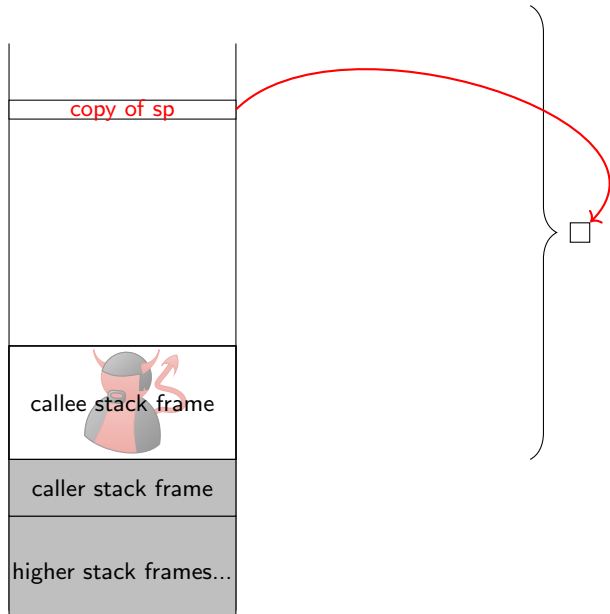
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Reasoning About a Machine with Local Capabilities

└ Calling Convention (Continued)

1. Stack is basically the only place we can store local capabilities.
2. Make sure that untrusted code don't "sneak" capabilities between calls on the stack
3. Clear stack and argument registers

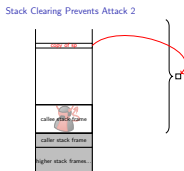
Stack Clearing Prevents Attack 2



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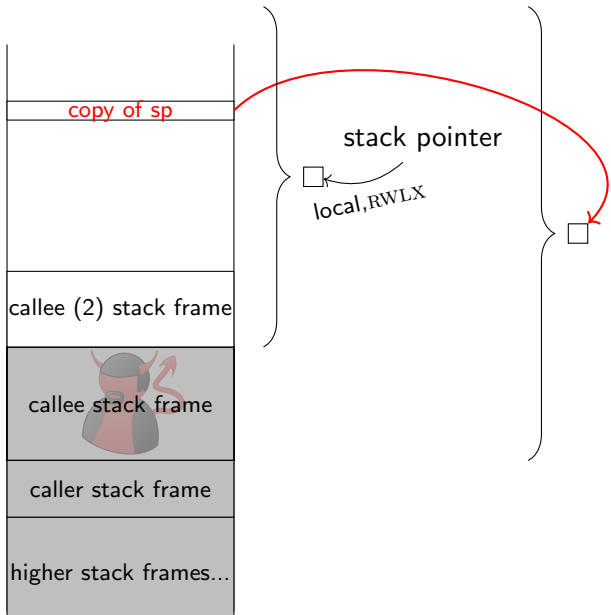
Reasoning About a Machine with Local Capabilities

Stack Clearing Prevents Attack 2



1. Let's see that the addition to the CC prevents attack 2.
2. The untrusted code has been called. It calls the well-behaved code.
3. The well-behaved code does its thing, but this time it clears the stack overwriting the old stack pointer the untrusted code had saved for later.
4. The untrusted code starts running, but it does not have an old stack pointer available only the one given to them by the well-behaved code

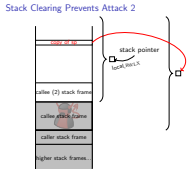
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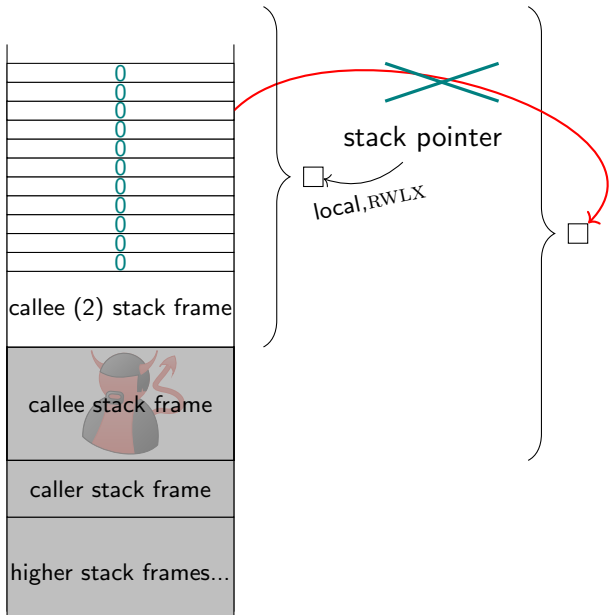
Reasoning About a Machine with Local Capabilities

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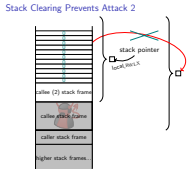


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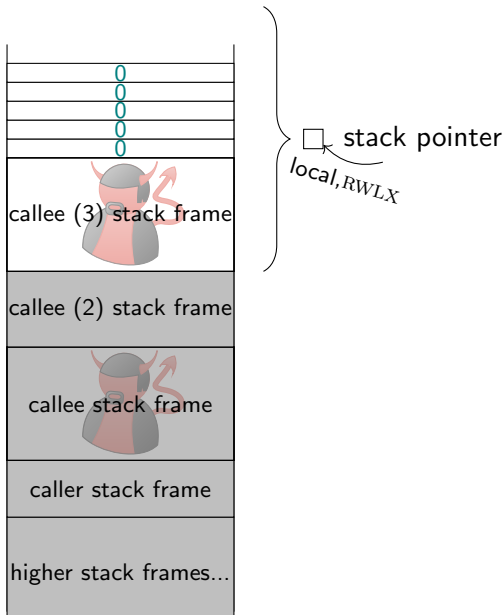
Reasoning About a Machine with Local Capabilities

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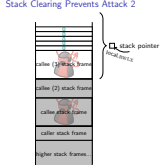
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Reasoning About a Machine with Local Capabilities

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- ## Reasoning About a Machine with Local Capabilities

(Full) Calling Convention

- ▶ Initially:
 - ▶ Stack capability local capability with read, write-local, and execute authority.
 - ▶ No global write-local capabilities on the machine.
- ▶ Prior to returning to untrusted code:
 - ▶ Clear the stack.
 - ▶ Clear non-return registers.
- ▶ Prior to calls to untrusted code:
 - ▶ Push activation record to the stack and create enter-capability
 - ▶ Restrict the stack pointer to the unused part and clear that part.
 - ▶ Clear non-argument registers.
- ▶ Only invoke global call-backs.
- ▶ When invoked by untrusted code
 - ▶ Make sure the stack pointer has read, write-local and execute authority.

- 11/19

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Reasoning About a Machine with Local Capabilities

└ Formalizing the Guarantees of a Capability Machine

- How can we be sure the calling convention works?

1. How can we be sure the calling convention works?
2. Specifically, if we have a program that interacts with untrusted code using the calling convention, how do we formally show correctness of the program.
3. We need a formal statement about the guarantees provided by the capabilities including the specific guarantees for local capabilities.
4. Traditionally syntactic very syntactic (e.g. reference graph), does not take into account what the program does with its capabilities.
5. We have defined a logical relation which also give us a statement about the guarantees provided by the capability machine.

- How can we be sure the calling convention works?
- Unary step-indexed Kripke logical relation over recursive worlds
 - Statement of guarantees provided by the capability machine

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Reasoning About a Machine with Local Capabilities

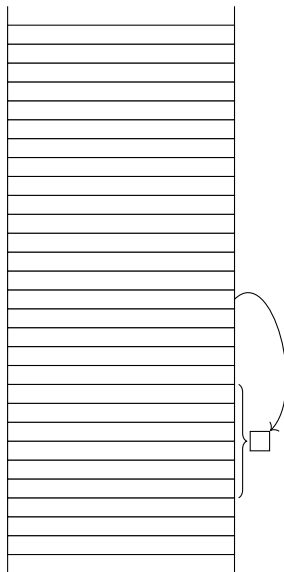
└ Formalizing the Guarantees of a Capability Machine

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4. Traditionally syntactic very syntactic (e.g. reference graph), does not take into account what the program does with its capabilities.
5. We have defined a logical relation which also give us a statement about the guarantees provided by the capability machine.
6. Calling convention main application, but it is general
7. In the following: give some intuition about what a LR looks like for a capability machine

Worlds, Safe Values, and Step-Indexing

- ▶ Capabilities represent bounds on executing code

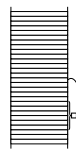


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Reasoning About a Machine with Local Capabilities

└ Worlds, Safe Values, and Step-Indexing

1. Compared to standard assembly language, capabilities executing code has access to represent bound.
2. That is, the capabilities the executing code has access to.
3. What exactly is the bound on. In the system we have considered, no I/O, so memory invariants.
4. Take what the program does into account - allows more fine-grained than simply "read/write"

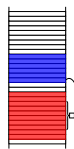


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-
- A vertical stack of 20 rectangular cells. The top 10 cells are white. The next 8 cells are blue. The next 6 cells are red. The bottom 2 cells are white. A bracket on the right side groups the 8 blue cells and the 6 red cells. A small square box with a cross inside is positioned to the right of the bracket, with a curved arrow pointing from the bracket to it.

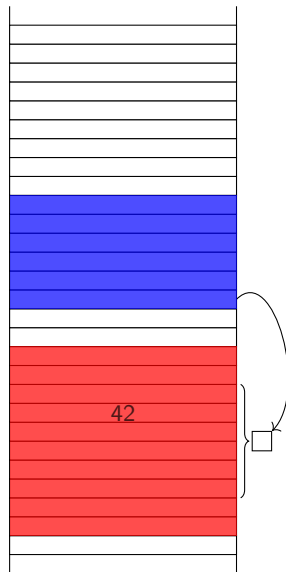
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5. The collection of invariants is what we call the world (and it can be thought of as a model of the memory)
6. Here colored region represents invariant. A simple invariant could be a specific address contains 42.



Worlds, Safe Values, and Step-Indexing

- ▶ Capabilities represent bounds on executing code
- ▶ World, W
 - ▶ Collection of invariants

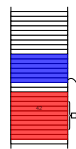


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Reasoning About a Machine with Local Capabilities

- └ Worlds, Safe Values, and Step-Indexing

1. Compared to standard assembly language, capabilities executing code has access to represent bound.
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5. The collection of invariants is what we call the world (and it can be thought of as a model of the memory)
6. Here colored region represents invariant. A simple invariant could be a specific address contains 42.
7. We are interested in all the words on the machine that preserve these invariants, so

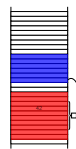


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-
- A diagram of a memory stack represented as a vertical column of cells. The stack is divided into three main sections: a top section of 10 white cells, a middle section of 10 blue cells, and a bottom section of 10 red cells. A bracket on the right side of the red section is labeled with the number 42. A curved arrow points from the right side of the blue section to a small square box, which is connected to the bracket on the red section.

└ Worlds, Safe Values, and Step-Indexing

1. We are interested in all the words on the machine that preserve these invariants, so
2. We define a predicate with theses values. Whether a capability violates invariants obviously depend on the invariants, so the predicate has to be world-indexed.
3. Let's see some examples:

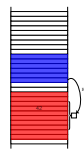


2018-04-15

-
- A vertical stack of memory cells. The top 10 cells are white. The next 10 cells are blue. Below these is a white cell, followed by a red segment of 12 cells. The number '42' is written in the middle of the red segment. A bracket on the right side of the red segment is connected by a curved arrow to a small square box labeled 'R'.

Worlds, Safe Values, and Step-Indexing

- ▶ Capabilities represent bounds on executing code
- ▶ World, W
 - ▶ Collection of invariants
- ▶ Predicate for safe values w.r.t world, $V(W)$



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4. If the capability on the slide has read authority, then it cannot violate the simple invariant.

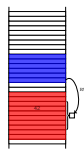
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- The diagram shows a vertical stack of memory cells. The top 10 cells are blue, and the bottom 42 cells are red. A bracket on the right side of the red region is labeled '42'. A curved arrow labeled 'RW' points to a small square box at the top of the red region, indicating the current read/write position.

└ Worlds, Safe Values, and Step-Indexing

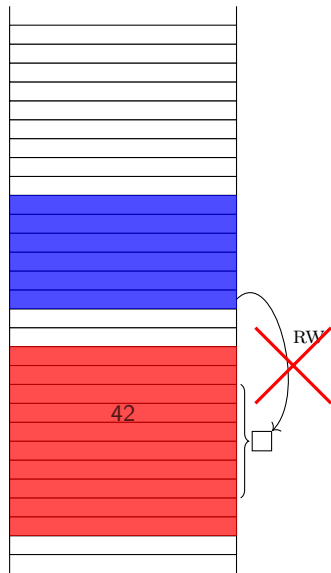
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5. If the capability on the slide also has write authority, then it can violate the invariant by simply overwriting that address with a different number. Not safe.

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Worlds, Safe Values, and Step-Indexing

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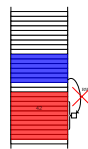
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Reasoning About a Machine with Local Capabilities

└ Worlds, Safe Values, and Step-Indexing

Worlds, Safe Values, and Step-Indexing

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- ▶ Predicate for safe values w.r.t world, $\mathcal{V}(W)$



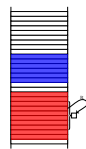
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3. Let's see some examples:
4. If the capability on the slide has read authority, then it cannot violate the simple invariant.
5. If the capability on the slide also has write authority, then it can violate the invariant by simply overwriting that address with a different number. Not safe.
6. Generally speaking a capability that can read is safe when it only can read safe words. What happens when it is stored at an address that it has authority over itself?
7. It is safe only if it can read only safe values which requires it to be safe.

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-
- A vertical stack of rectangular blocks representing memory. The stack is divided into three main color-coded regions: a top section of white blocks, a middle section of blue blocks, and a bottom section of red blocks. To the right of the red section, a bracket groups several blocks, with an arrow pointing to a small square box labeled 'R'.

- └ Worlds, Safe Values, and Step-Indexing

1. It is safe only if it can read only safe values which requires it to be safe.
2. Need to take a fixed-point. Made possible by step-indexing.



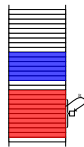
2018-04-15

-
- A vertical stack of horizontal bars representing memory. The stack is divided into several sections. From top to bottom: 10 white bars, 5 blue bars, 1 white bar, 10 red bars, 1 white bar, and 2 white bars. A bracket on the right side of the red section is labeled 'R'. An arrow points from the 'R' label to a small square box, which is positioned between the red and the final white section.

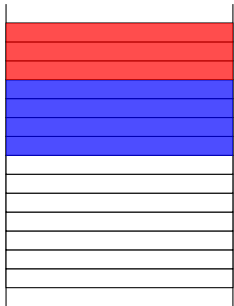
- └ Worlds, Safe Values, and Step-Indexing

1. It is safe only if it can read only safe values which requires it to be safe.
2. Need to take a fixed-point. Made possible by step-indexing.
3. Related to similar issue for languages with recursive types and ML-like references.

- ▶ Capabilities represent bounds on executing code
- ▶ World, W
 - ▶ Collection of invariants
- ▶ Predicate for safe values w.r.t world, $\mathcal{V}(W)$
 - ▶ Recursively definition



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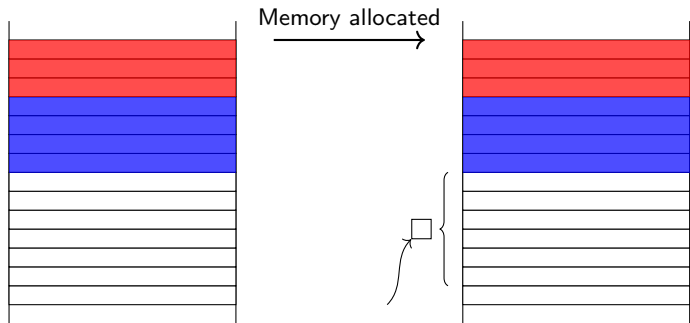
Reasoning About a Machine with Local Capabilities

- Future Worlds and Invariants, and Recursive Worlds

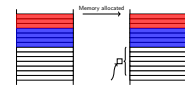


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- ▶ Memory evolves over time

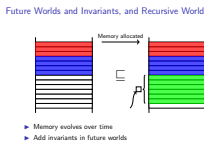
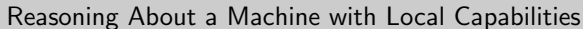


- ▶ Memory evolves over time

- Future Worlds and Invariants, and Recursive Worlds

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2. Notion called future worlds. Allow us to add invariants.

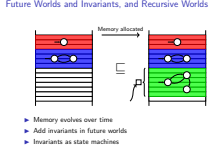
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- ## Future Worlds and Invariants, and Recursive Worlds

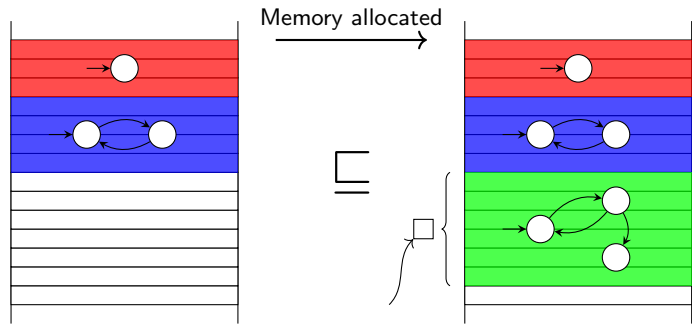
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4. Each state is associated with a predicate of all memories that respect the invariant.

Future Worlds and Invariants, and Recursive Worlds

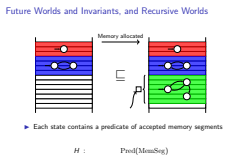


- Each state contains a predicate of accepted memory segments

$H :$ $\text{Pred}(\text{MemSeg})$

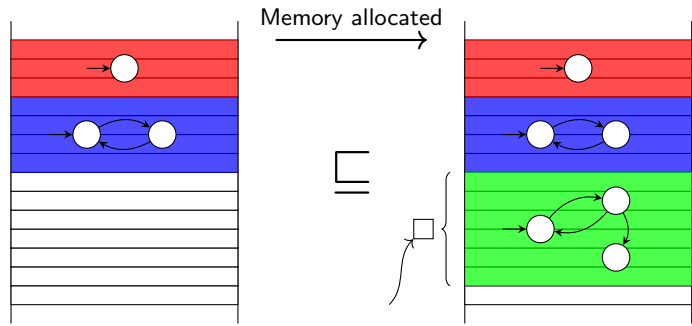
Reasoning About a Machine with Local Capabilities

Future Worlds and Invariants, and Recursive Worlds



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Future Worlds and Invariants, and Recursive Worlds



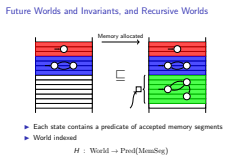
- ▶ Each state contains a predicate of accepted memory segments
- ▶ World indexed

$$H : \text{World} \rightarrow \text{Pred}(\text{MemSeg})$$

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Reasoning About a Machine with Local Capabilities

Future Worlds and Invariants, and Recursive Worlds



1. Like languages with ML-references, memory changes over time. Example, if we are in this memory with two invariants and more is memory allocation. World need to cope with this.
2. Notion called future worlds. Allow us to add invariants.
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4. Each state is associated with a predicate of all memories that respect the invariant.
5. We want to express "all memories with safe values". World dependent, so the predicate needs to be world indexed.
6. Worlds with invariants with state machines with predicates that are world indexed - circular definition.
7. Resolved using standard techniques from the literature (essentially advanced step-indexing).

Local Capabilities

\mathfrak{f} is unknown code and \mathfrak{c} is a capability.

 $f(c);$
$$f(1)$$

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Reasoning About a Machine with Local Capabilities

- Local Capabilities

Local Capabilities

E is unknown code and c is a capability.

 $f(c):$ $\in (1)$

1. Now consider how local capabilities affect all this.
2. Consider this simple example, first f is called with capability c as an argument. Then f is called with unit.
3. What may we assume about c in the second invocation of f ?
4. Depends on c !

Local Capabilities

\mathfrak{f} is unknown code and c is a capability.

 $f(c);$
$$f(1)$$

- ▶ c global \Rightarrow available in second invocation of f

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Reasoning About a Machine with Local Capabilities

- Local Capabilities

1. (Cont) Depends on `c`!
2. If `c` is global, then it can be stored on the heap, so it needs to remain safe for the remainder of the execution. When `f` is invoked `c` must still be safe.

\bar{c} is unknown code and c is a capability.

E. J. M.

495

1997

- ▶ c global \Rightarrow available in second invocation of E

Local Capabilities

\mathfrak{f} is unknown code and \mathfrak{c} is a capability.

 $f(c);$
$$f(1)$$

- ▶ c global \Rightarrow available in second invocation of f
- ▶ c local \Rightarrow not available in second invocation of f

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Reasoning About a Machine with Local Capabilities

└ Local Capabilities

Local Capabilities

ε is unknown code and c is a capability

E. J. M.

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- ▶ c global \Rightarrow available in second invocation of E
- ▶ c local \Rightarrow not available in second invocation of E

1. (Cont) Depends on c!
2. If c is global, then it can be stored on the heap, so it needs to remain safe for the remainder of the execution. When f is invoked c must still be safe.
3. If c local, then CC dictates clear all the places c may reside, so in the second invocation c need not remain safe.
4. Need two future world relations. In both, global capabilities must remain safe. In one local capabilities need not. We have public and private future world relation.

Local Capabilities

\mathfrak{f} is unknown code and \mathfrak{c} is a capability.

 $f(c);$
$$f(1)$$

- ▶ c global \Rightarrow available in second invocation of f
- ▶ c local \Rightarrow not available in second invocation of f

Lemma (Double monotonicity of safety predicate)

- ▶ If $(n, w) \in \mathcal{V}(W)$ and $W' \sqsubseteq^{pub} W$ then $(n, w) \in \mathcal{V}(W')$.
- ▶ If $(n, w) \in \mathcal{V}(W)$ and $W' \sqsubseteq^{priv} W$ and w is not a local capability, then $(n, w) \in \mathcal{V}(W')$.

2018-04-15

Reasoning About a Machine with Local Capabilities

└ Local Capabilities

Local Capabilities

\bar{c} is unknown code and c is a capability.

Edith

$$\in (1)$$

- ▶ c global \Rightarrow available in second invocation of E
- ▶ c local \Rightarrow not available in second invocation of E

Lemma (Double monotonicity of safety predicate)

- ▶ If $(n, w) \in V(W)$ and $W' \sqsupseteq^{pub} W$ then $(n, w) \in V(W')$
- ▶ If $(n, w) \in V(W)$ and $W' \sqsupseteq^{priv} W$ and w is not a local capability, then $(n, w) \in V(W')$.

1. This double monotonicity lemma expresses the assumptions we can make, namely in public future world all capabilities remain valid and in private future worlds local capabilities need not remain valid.
2. In the example this means that if c is local, then it is okay to invoke f a second time in a private future world as c need not be safe anymore.
3. On the other hand, if c is global, then the invocation of f must be in a public future world, so c remains safe.
4. Related to public private future worlds, state machines with pub/priv transitions

2018-04-15

- ## Reasoning About a Machine with Local Capabilities

└ Fundamental Theorem of Logical Relations

- ▶ General statement about the guarantees provided by the capability machine.
- ▶ Intuitively: any program is safe as long as it only has access to safe values.

Theorem (Fundamental theorem (simplified))

If

$$(n, \{b, a\}) \in \text{readCond}[g](W)$$

then

$$(n, ((\text{rx}, g), b, a)) \in \mathcal{E}(W)$$

Theorem (Fundamental theorem (simplified))

If

$$(n, (b, e)) \in \text{readCond}(g)(W)$$

then

$$(n, ((\text{RX}, g), b, e, a)) \in \mathcal{E}(W)$$

then

$$(n, ((RX, g), b, e, a)) \in \mathcal{E}(W)$$

1. Now for the formal statement about guarantees
2. readCond: only safe values in the interval.
3. E-relation: when capability used as the pc with register file and memory respecting the world, then the execution respects the memory invariants.
4. In other words, take an arbitrary capability that only has access to safe values, then the memory invariants are preserved when we use it for execution.
5. The instructions it execute don't matter only the authority it can use.

“Awkward Example”

```
let x = ref 0 in
  λf. (x := 0;
       f();
       x := 1;
       f();
       assert (!x == 1))
```

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Reasoning About a Machine with Local Capabilities

└ “Awkward Example”

"Awkward Example"

```
let x = ref 0 in
  Af. (x := 0;
      f();
      x := 1;
      f();
      assert(!x == 1))
```

1. known from the literature
2. in ML difficult to reason about as callback f can be the closure it self. (so x can be either)
3. assert may fail if calls not well-bracketed
4. can do more things to attack well-bracketedness low-level. Context need not follow CC, so well-behaved code cannot rely on behavior of untrusted code.
5. We have proven a faithful translation correct. That is the assert never fails. Notice, dynamic checks, so machine can fail, but we set it up, so we can distinguish this from assertion failure.
6. More semantic statement of guarantees allow us to prove it.

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- ## Reasoning About a Machine with Local Capabilities

Conclusion

- ▶ Capability machines can guarantee properties of high-level languages.
- ▶ We developed a calling convention that ensures well-bracketedness and local-state encapsulation.
- ▶ We define a unary step-indexed Kripke logical relation over recursive worlds.
 - ▶ Formal statement about guarantees provided by capability machine.
 - ▶ Allows reasoning about programs on capability machine.
- ▶ We apply it on the "awkward example".

Thank you!